

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1131	716/8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:30
L2	1684	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:30
L3	1495	716/2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:30
L4	4	(716/8).ccls. and ((memory or semiconductor) adj chip) and (modules) and (data adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:32
L5	3	(716/1).ccls. and ((memory or semiconductor) adj chip) and (modules) and (data adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:32
L6	6	(716/2).ccls. and ((memory or semiconductor) adj chip) and (modules) and (data adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:32
L7	56	("716"/\$).ccls. and ((memory or semiconductor) adj chip) and (modules) and (data adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:32
L8	1897	((memory or semiconductor) adj chip) and (modules) and (data adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:33
L9	36	((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:35
L10	25	(select\$3 and activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:37

L11	1	(select\$3 adj probabilit\$3) and (activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:38
L12	0	(associat\$3 adj indi\$3) and (activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:39
L13	0	(associated adj indices) and (activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:40
L14	5	(indices) and (activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:42
L15	1	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (assess\$4 same temperature)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:43
L16	12	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (associat\$4 same register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:46
L17	1	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (associat\$4 same (activ\$4 adj group) same register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:47
L18	1	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same statistical\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:48
L19	12	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same alternat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:52

L20	0	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same (alternat\$4 near group\$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:50
L21	0	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same (alternat\$4 near groups))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:51
L22	1	(activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:52
L23	1	((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:52
L24	1	((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:54
L25	1	((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same alternat\$4)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:53
L26	0	(716/8).cccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same alternat\$4)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:54
L27	0	(716/1).cccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same alternat\$4)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:54

L28	0	(716/2).ccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same alternat\$4)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:56
L29	0	(716/8).ccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:55
L30	0	(716/1).ccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:55
L31	0	(716/2).ccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:55
L32	1	("716"/\$).ccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and ((data adj interchange) same (alternat\$4))).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:55
L33	1	("716"/\$).ccls. and ((activat\$3 and perform\$3) and ((memory or semiconductor) adj chip) and (modules) and (common adj (data adj bus)) and (group or set) and (select\$4 same alternat\$4)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/19 13:56